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REMARKS

Claims 1-20 have presented in the above-identified ì U.S. Patent Application. 2 3 Claims 2, 5-7, 11, 16 and 17 have been withdrawn from 4 consideration in response to a Restriction Requirement as 5 described in Paragraph 2 if the Office Action. 6 7 Claims 21-23 have been added by this Amendment A. 8 9 Claims 1, 3, 4, 8-10, 12-15 and 18-23 are in the 10 Application and reconsideration of the Application is 11 hereby respectfully requested. 12 13 Referring to Paragraph 3, Claim 12 has been objected 14 to because of a Claim informality kindly pointed out by 15 Examiner. Claim 12 has been amended to remove the Claim 16 informality. Therefore, objection to Claim 12 has been 17 answered by amendment. 18 19 Referring to Paragraph 4, Claims 15, 18, 19, and 20 20 have been rejected under 35 U.S.C. 112, second paragraph, 21 as being indefinite for failing to particularly point out 22 and distinctly claim the subject matter which applicant 23 regards as the invention. The ambiguity in the Claims 24 identified by Examiner, an inconsistency in the use of 25

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"signal group/groups", is believed to have been corrected

by the amendments to the Claims. Therefore, rejection of

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Claims 15, 18, 19, and 20 under 35 U.S.C. 112, second
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   paragraph, has been answered by amendment.
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        Referring to Paragraph 7 of the Office Action, claims
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   1, 3, 9, 10, 12-14, 15, and 19 have been rejected under 35
   U.S.C. 102(e) as being anticipated by U.S. Publication
   2005/0003781 issued in the name of Kunz al (hereinafter
   referred to as Kunz). Referring to Paragraph 9 of the
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   Office Action, Claims 4, 8, and 20 have been rejected under
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    35 U.S.C, 103(a) as being unpatentable over Kunz (cited
10
   above) in view of U.S. Patent 4,989,204 issued in the name
11
    of Shimizu et al (hereinafter referred to as Shimizu).
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13
         Before considering the relationship of the references
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    and the Claims, the present invention, as defined by the
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    amended Claims, will be summarized. Almost from the
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    beginning of the fabrication of integrated circuits, the
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    goal has been to include in the chips an increasing number
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    of components. This increasing number of components has
19
    been possible because of the decreasing dimensions of the
20
    individual components. In addition, the number of bits
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    being simultaneously processed has been steadily
22
    increasing. For example, data groups being processed have
23
    expanded from 4 bits to 128 bits. With the decreasing
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    component dimensions along with the increasing component
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    complexity, the problem of entering signal groups into the
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    semiconductor chips has been an increasingly difficult
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    problem. The conducting paths themselves have approached
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    the limits as to what can be physically manipulated. A
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    practical technique of transferring logic signal groups
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from chip-to-chip has proven an extremely difficult
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- technological problem. Various techniques, such as the 2
- multiplexing of chip interface terminal have been used with 3
- mixed success.

The solution to this problem as disclosed in the 6

present Application and described in the Claims of the 7

Application is to provide a non-conducting path between the 8

semiconductor chips. This non-conducting path removes the 9

problem of the physical conducting paths at the cost of an 10

increase in the number of components used to effect a 11

wireless transfer of signal groups. However, the component 12

miniaturization has made it more convenient to accommodate 13

additional components on each chip as compared to providing 14

additional chip-to-chip conducting paths. The Claims, as 15

amended, clearly indicate that the signal transmitting 16

component and the signal receiving component are located on 17

the same integrated circuit board or the same semiconductor 18

substrate. This relationship is clearly present in the 19

independent Claims 1, 9, and 15. Furthermore, the 20

decreased dimensions of the distance between the 21

semiconductor chips themselves require relatively little 22

power to transfer wireless signals there between. In 23

addition, once the receiving and the transmitting 24

components have been designed and tested, the incorporation 25

of these components for wireless transmission can be 26

incorporated in a catalog of chips that can be fabricated 27

28 conveniently.

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Referring once again to the references, the Kunz 1 reference describes and claims a wireless receiver unit, 2 the thrust of the invention being the technique for 3 processing the incoming wireless signals. Nowhere in the Kunz reference is there any teaching of the transmission of information between two components on a circuit board. 6 Indeed, nowhere in the Kunz reference is any indication of the source or the location of the wireless transmitting 8 units that provide the signals for the disclosed circuit to manipulate. Therefore, the invention sought to be 10 protected by the amended Claims is not claimed, disclosed 11 or even suggested by the Kunz reference. Consequently, 12 rejection of Claims 1, 3, 9, 10, 12-14, 15, and 19 is 13 respectfully traversed. 14 15 Referring to the Shimizu reference, this reference 16 discloses apparatus for improving the receipt of a wireless 17 signal from a transmitter that is crossing boundaries of 18 cell phone area. Clearly the movement of the wireless 19 transmitter is unrelated to the transfer of signal groups 20 from one semiconductor chip to a second semiconductor chip 21 on the same substrate or on the same integrated circuit. 22 Even when the transmitting and receiving chips are on 23 different circuit boards, the position of the transmitting 24 wireless unit is fixed relative to the receiving wireless 25 unit. Thus, the Shimizu reference in no way makes up for 26 the shortcomings of the configuration described by the Kunz 27 reference. The references, either alone or together, do 28 not relate to the configuration of the Patent Application 29 30 in which, to avoid the use of conducting paths, signal

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- I groups are transmitted between chips on a circuit board or
- integrated circuit board. Therefore, rejection of Claims
- 3 4, 8, and 20 under 35 U.S.C. 103(e) as being unpatentable
- 4 over Kunz in view of Shimizu is respectfully traversed.

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- 6 Consequently, it is believed that Claims 1, 3, 4, 8-
- 7 10, 12-15 and 18-23, all the Claims now in the Application,
- 8 are in condition for allowance.

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CONCLUSION

- In view of the foregoing discussion and the foregoing
- 2 amendments, it is believed that Claims 1, 3, 4, 8-10, 12-15
- 3 and 18-23 are now in condition for allowance and allowance
- 4 of Claims 1, 3, 4, 8-10, 12-15 and 18-23 is respectfully
- 5 requested. Applicant(s) hereby respectfully request a
- 6 timely Notice of Allowance be issued for this Application.

Respectfully submitted,

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